

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institution with permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru – 521 356, Krishna Dist., A.P.

I B.TECH - II SEMESTER (R23) SECOND MID TERM EXAMINATIONS

Subjective Examination - 10.00 am to 11.30 am.

Objective Examination – 12.00 Noon to 02.00 pm

TIME TABLE

9th June 2026

DATE & DAY	CE	EEE	ME	ECE	CSE	IT	AI&DS	IOT	CSE (AI&ML)
15.06.2026 (Monday)	Engineering Chemistry (CH4503)	Chemistry (CH4501)	Engineering Chemistry (CH4503)	Chemistry (CH4501)	Engineering Physics (PH4501)	Engineering Physics (PH4501)	Engineering Physics (PH4501)	Chemistry (CH4501)	Chemistry (CH4501)
16.06.2026 (Tuesday)	Communicative English (EG4501)	Communicative English (EG4501)	Communicative English (EG4501)	Communicative English (EG4501)	Timings: 9.15 am to 11.15 am	Timings: 1.45 pm to 3.45 pm	Timings: 11.30 am to 01.30 pm	Communicative English (EG4501)	Communicative English (EG4501)
					Engineering Graphics (ME4501)	Engineering Graphics (ME4501)	Engineering Graphics (ME4501)		
17.06.2026 (Wednesday)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)	Differential Equations & Vector Calculus (MA4502)
18.06.2026 (Thursday)	Basic Civil & Mechanical Engineering (CM4501)	Basic Civil & Mechanical Engineering (CM4501)	Basic Civil & Mechanical Engineering (CM4501)	Basic Civil & Mechanical Engineering (CM4501)	Basic Electrical & Electronics Engineering (EE4501)	Basic Electrical & Electronics Engineering (EE4501)	Basic Electrical & Electronics Engineering (EE4501)	Basic Civil & Mechanical Engineering (CM4501)	Basic Civil & Mechanical Engineering (CM4501)
19.06.2026 (Friday)	Engineering Mechanics (CE4501)	Electrical Circuit Analysis –I (EE4505)	Engineering Mechanics (CE4501)	Network Analysis (EE4503)	Data Structures (CT4504)	Data Structures (CT4504)	Data Structures (CT4504)	Network Analysis (EE4503)	Data Structures (CT4504)

Note:1. Seating arrangement for Subjective examinations will be notified before the commencement of the examination by the respective departments.

2. Computer Labs for objective examination will be notified by the respective departments

M. Suresh
Vice-Principal
(Academics)

Copy to: All Notice Boards

HODs – CE/EEE/ME/ECE/CSE/IT/AI&DS/IoT/CSE(AI&ML)/BS&H/ENG/MBA for faculty Circulation/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)